



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

JOHANNES R. GERARDUS DE VRIES

Application No.: 08/966,954

Filed: November 10, 1997

For: **Data Processing Circuit, Multiplier Unit
with Pipeline, ALU and Shift Register Unit
for Use in a Data Processing Circuit**

Art Group: 2183

Examiner: Petranek, Jacob Andrew

Confirmation No.: 6312

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 3/20/08

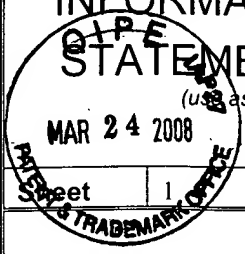

Chui-Kiu Teresa Wong, Reg. No. 48,042

1279 Oakmead Parkway
Sunnyvale, CA 94085-4040
Telephone: (408) 720-8300

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				Application Number	08/966,954
				Filing Date	11/10/1997
				First Named Inventor:	Johannes R. Gerardus de Vries
				Art Unit	2183
				Examiner Name	Jacob Petranek
Sheet 1	of 1	Attorney Docket Number		6211.P001	
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
		Office Action mailed 3/23/2007. 11/347,194, filing date of 2/2/2006. 17 pages.			
		Office Action mailed 8/9/2007. 11/347,194, filing date of 2/2/2006. 22 pages.			
		Advisory Action mailed 11/21/2007. 11/347,194, filing date of 2/2/2006. 3 pages.			

Examiner Signature	Date Considered
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